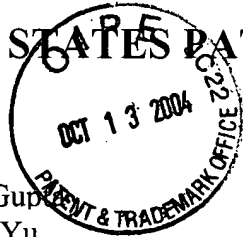


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Gagan V Gupta
Mengchen Yu



Serial No. :

09/927,139

Group Art Unit :

2124

Filed :

August 10, 2001

Examiner :

Ngo, Chuong D.

For :

Floating Point/Integer Divide &
Square Root Processor

Atty Docket :

L13.12-0159 / 01-074

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Mark Salvatore

October 13, 2004

Date

A handwritten signature in black ink, appearing to read "Mark Salvatore".

Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Sandeep Jaggi".

Sandeep Jaggi

Reg. No. 43,331

Date: 10/11/04